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N THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Nobuaki HASHIMOT,O

Application No.: 09/486,556

Filed: February 29, 2000

ATTN: Application Branch

Docket No.: 105029

(

NOTIFICATION OF ERRORS IN FILING RECEIPT

CIRCUIT BOARD AND ELECTRONIC INSTRUMENT

SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURE THEREOF,

Director of the U.S. Patent and Trademark Office Washington, D.C. 20231

Sir:

For:

Attached is a photocopy of the original filing receipt on which errors have been corrected in red. These errors are being brought to the attention of the Patent and Trademark Office so that it may correct its records. No corrected filing receipt is requested in view of the fact that the errors will not be transferred to the Letters Patent upon its issuance under the Patent and Trademark Office policy announced at 1220 O.G.42 (March 9, 1999). Please contact the undersigned if there is any question about the accuracy of these corrections.

Respectfully submitted,

James A. Oliff

Registration No. 27,075

Thomas J. Pardini

Registration No. 30,411

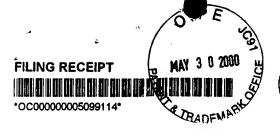
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TOT APPLICATION NUMBER FILING DATE **GRP ART UNIT** FIL FEE REC'D ATTY.DOCKET.NO **DRAWINGS** IND CLAIMS CLAIMS 09/486,556 02/29/2000 3729 1002 105029 7 29 2

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Applicant(s)

SUWA-SHI

NOBUAKI HASHIMOTO, NAGANO-KEN, JAPAN;

Continuing Data as Claimed by Applicant

THIS APPLICATION IS A 371 OF PCT/JP99/03417 06/25/1999

Foreign Applications

JAPAN 10-201246 07/01/1998

If Required, Foreign Filing License Granted 05/05/2000

Title

SEMICONDUCTOR DEVICE/METHOD OF MANUFACTURE/CIRCUIT BOARD/AND

ELECTRONIC DEVICE

Preliminary Class

029

Data entry by : BARRETO, NGA

Team: OIPE

Date: 05/05/2000

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